



#7 EOT/Andot.B  
5-6.03  
Makish  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
Reddy

Art Unit: 2811

Examiner: M. TRAN

Serial No. 10/011,863

Docket No. SMA-001.1D

Filed: 11/12/01

For: INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND METHOD FOR  
MAKING SAME

Honorable Commissioner  
of Patents and Trademarks  
Mail Stop Non-Fee Amendment  
Post Office Box 1450  
Alexandria, VA 22313-1450

Morgan Hill, California  
April 26, 2003

AMENDMENT

Dear Sir:

In response to the Office Action mailed 12/26/2002, please amend the above  
identified case as follows.

IN THE CLAIMS

RECEIVED  
MAY -2 2003  
TECHNOLOGY CENTER 2800

01 FC:2251  
55.00 CH  
05/06/2003 CHOLISH 00000001 060932 10011863

- 1 1. (Amended) An integrated circuit structure comprising:
- 2 a plastic substrate;
- 3 a layer of silicon dioxide or silicon nitride having a thickness such that little
- 4 or no differential strain between the substrate and said layer occurs at any
- 5 temperature in the normal operating temperature range of said integrated circuit;
- 6 an antenna conductor which is bonded onto, integrated onto or printed

BI